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Title of Invention	BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE							
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Date :	2002-02-22							
First Named Applicant:	Wolrich Gilbert							
Confirmation Number:	1429							
Attorney Docket Number:	10559-305US1							
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<table border="1"><thead><tr><th data-bbox="131 993 773 1031">Submitted By:</th><th data-bbox="773 993 1110 1031">Elec. Sign.</th><th data-bbox="1110 993 1414 1031">Sign. Capacity</th></tr></thead><tbody><tr><td data-bbox="131 1031 773 1108">Scott C. Harris Registered Number: 32,030</td><td data-bbox="773 1031 1110 1108">/SCH/</td><td data-bbox="1110 1031 1414 1108">Attorney</td></tr></tbody></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Scott C. Harris Registered Number: 32,030	/SCH/	Attorney
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Documents being submitted:	Files
us-ids	10559-305US1_2-usidst.xml us-ids.dtd us-ids.xsl
Comments	
IDS # 2 of 4 for U.S. Application Serial No. 10/069,229	